

**Notice of References Cited**Application/Control No.  
09/920,978Applicant(s)/Patent Under  
Reexamination  
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2813

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4352724	10-1982	Sugishima et al	204/192
	B	US-4836887	06-1989	Daubenspeck et al	156/643
	C	US-5286344A	02-1994	Blalock et al	156/657
	D	US-US006140168A	10-2000	Tan et al	438/197
	E	US-US005814563A	09-1998	Ding et al	438/714
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 9-129608	05-1997	Japan	-----	H01L 21/302
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf SILICON PROCESSING for the VSLI ERA Vol. 1 Lattice Press 1986 Pages 551-557
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.